

Inside this issue:

- Happenings at the department
- Academia- Industry Events
- Industry Lectures
- Students' achievements
- Faculty page

ABOUT THE DEPARTMENT

VISION

Global leadership and excellence in academics and research in electronics and communication engineering.

MISSION

- To deliver a competitive academic curriculum for intellectual growth and Multi-disciplinary skill acquisition.
- Promoting quality education, scientific awareness for future endeavors in higher education and career in electronics and communication industry.
- To cater to the need of promoting leadership and professionalism with human values

The Department of Electronics & Communication Engineering (ECE) at Manipal University Jaipur provides best learning environment for imparting scientific knowledge to develop practical design engineers. Keeping pace with latest technological trends and expectations of society, the department has adapted itself with continuous involvement of its stakeholders. The faculty at ECE always motivate and nurture students' inclination towards research and development with access to sophisticated PCB prototyping machine and latest software for publications in conferences and Journals of repute. The department offers 2 Undergraduate programs in Electronics and Communication Engineering and Electronics Engineering (VLSI Design & Technology); 1 Postgraduate program in VLSI and Embedded System Design and Doctorate program. The well-equipped laboratories and resourceful library at MUJ constitute the best info-oriented junction for the students.

The flexible curriculum at MUJ offers a diversified course bucket to pursue students' interest in communication systems, semiconductor and optical devices, electronic circuits, together with assembly languages, programming languages, and embedded systems, thereby opening gates to interdisciplinary growth with a wide choice of career options to pursue their interest. The ECE graduates have been spread successfully in software as well as hardware industry.

HAPPENINGS AT THE DEPARTMENT



MUJ Optica Chapter was proud organizing partner of Winter School on Computational Optics and Photonics CompOpt 2025 held at Mahindra University Hyderabad during January 2-6, 2025. CompOpt 2025 was sponsored by Optica and Ansys. As the convenor of CompOpt 2025, I am happy to share that about 35 PhD scholars and Masters students participated in the Winter School from all over India including research scholars from IITM, IITR, IITJ, IITH, NCL, TIFR, CSIO, CUSAT, UoC, MIT-WPU, IIST etc. There were technical sessions from Dr. Arti Agrawal, Dr. Manish Tiwari, Dr. Kiran Hiremath, Dr. Kamna Pande, Dr. Somnath Ghosh and Dr. Jayasri Dontabhaktuni. There were industry sessions from Ansys and COMSOL during the Winter School.

Expert Talks

Lithography Techniques in Device Miniaturization

Organized by
Multiscale Simulation Research Center (MSRC)
In Association with
School of Engineering
&
**Departments of Mechanical Engineering, and
Department of Electronics and Communication Engineering**
Manipal University Jaipur

Expert



Dr. Prithvi Basu
Research Engineer
AppleFab Nanofabrication Facility
Department of Electrical and Computer Engineering
Texas A&M University
Mailstop 3253 TAMU
College Station, TX 77843

Date, & Time
Date: March 21, 2025
Time: 9:30 AM-10:30 AM

Please join the meeting via
<https://bit.ly/3daz715p>

For More Information, Please Contact
Dhanshwar Malhotra, Ph.D.
Assoc. Professor (Senior Faculty)
Department of Mechanical Engineering
Manipal University Jaipur
Mobile: 9619474012
Email: dhanshwar.malhotra@mujaipur.manipal.edu

Himanshu Choudhary, Ph.D.
Assoc. Professor (Senior Faculty)
Department of Electronics and Communication Engineering
Manipal University Jaipur
Mobile: 9619474012

A lecture by Dr Prithvi Basu is organized on Lithography Techniques in Device Miniaturization

GEETANJALI INSTITUTE OF TECHNICAL STUDIES (Affiliated to Rajasthan Technical University, Kota & Approved by AICTE, New Delhi)
"NAAC ACCREDITED 'A' GRADE INSTITUTE | NBA ACCREDITED ENGINEERING & MANAGEMENT PROGRAMS"

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
organizes

**5-DAY FACULTY DEVELOPMENT AND HANDS-ON TRAINING PROGRAM ON
FUNDAMENTALS AND RECENT ADVANCES IN
VLSI AND SEMICONDUCTOR
TECHNOLOGY**

EXPERTS



6th-10th February 2025
at 10:00 AM
ECE Seminar Hall

Dr Shilpi Birla was invited as an expert for 5-day FDP and Hands-on Training program on VLSI and Semiconductor Technology at Geetanjali Institute of Technical Studies, Udaipur

MANIPAL UNIVERSITY JAIPUR | **MTT-S** | **IEEE AP-S** | **AESS**
Antenna and Propagation Society

IEEE AESS, MTT-S & AP-S Student Branch Chapters
Department of ECE, SEEC, Manipal University Jaipur
Presents

Invited Talk on:
"Research and Innovation
in Antenna Design
Technology"

DATE: 30 DECEMBER 2024
TIME: 2:30 PM ONWARDS (IST)
**VENUE: BOARD ROOM 2ND-
FLOOR, 1 AB BLOCK, MUJ**

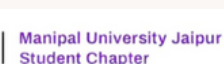


DR. ASHUTOSH KEDAR
SCIENTIST G, LRDE, DRDO
VICE-CHAIR, IEEE AP/MTT J
CHAPTER, BANGALORE

CONVENOR:
DR. DINESH YADAV

IEEE AESS, MTT-S and AP-S Department of ECE, Manipal University Jaipur hosted talk by Dr. Ashutosh Kedar, Sc 'G', (LRDE), DRDO, Bangalore, Vice-Chair, IEEE AP/MTT Jt. Chapter, Secretary, IEEE AESS Chapter, Bangalore on "Research and Innovation in Antenna Design Technology"

Student Chapters at ECE, MUJ



Academia- Industry Events



MANIPAL UNIVERSITY JAIPUR
(University under Section 2(f) of the UGC Act)

4 QUALITY EDUCATION **9 INDUSTRY INNOVATION AND INFRASTRUCTURE**

SEMISYNERGY 2025
CHIP INNOVATION & STARTUPS
(20-21 February, 2025)

Atal Incubation Centre - Manipal University Jaipur (AIC-MUJ)
welcomes Dignitaries

CHIEF PATRON
Mr. S. Vaitheeswaran
Chairperson, MUJ

PATRON
Dr. N.N. Sharma
President, MUJ

CO - PATRON
Dr. Karunkar A Kotegar
Pro-President, MUJ

Dr. Amit Soni
Registrar, MUJ

Dr. Nitu Bhatnagar
Provost, MUJ

Dr. Kuldip Singh Sangwan
Dean FoSTA, MUJ

Dr. Sandeep Joshi
CEO, MUJ-AIC

Mr. Sudhir Naik
(Founder, aishwarya, Treasurer IESA)

Dr. Pradeep Thaker
(Country Head & VP d-Matrix)

Mr. Varun Manwani
(Managing Director, SARAKA SEMICONDUCTORS PVT LTD)

Mr. Nikul Shah
(Founder & CEO INDESEMIC)

Dr. Satish Chand Tiwari
(CO-Founder, SOCIETAMP SEMICONDUCTORS PVT LTD)

Dr. Vineet Sahula
(Professor, H&B & B&S, Digital Infrastructure & Services, Malware National Institute Of Technology)

Mr. Nilesh Ranpura
(Senior Director Engineering, aishwarya)

Mr. Kunal Ghosh
(CO-Founder VLSYSYSTEMSDSIGN)

Dr. Wasi Uddin
(Senior Manager R&D and Training, KATRES SEMICON PVT LTD)

Organized By - AIC-MUJ, In Association with IESA and Department of ECE

Manipal University Jaipur (AIC-MUJ) is thrilled to host SEMI-SYNERGY 2025: Chip Innovation & Startups on 20-21 February 2025! This event brings together industry leaders, entrepreneurs, and innovators to drive the future of semiconductor technology and startup ecosystems. We are honored to welcome distinguished dignitaries who are shaping the semiconductor landscape:



MANIPAL UNIVERSITY JAIPUR **MUJ** **IESA** **CAE**

SEMISYNERGY 2025
CHIP INNOVATION & STARTUPS
presents
WORKSHOP ON RISC-V

RESOURCE PERSON
Mr KUNAL GHOSH

21 Feb. 2025 Registration Link 01:30 PM

Workshop on RISC-V by the renowned kunal ghosh (vlsisystemdesign.com) at SemiSynergy 2025: Chip Innovation & Startups!



Silicon to System exhibition by Dr Pradeep Thaker, VP and Country Head D-Matrix. This exhibition showcased various fabricated ICs from micrometer to nanometer.



Panel discussion on **"Bringing together academia, startups and industry"** was organized to enable holistic growth of this sunrise sector. Dr. K. S. Sangwan, Dr. Amit Soni, Dr. Pradeep Thaker, Dr. Vineet Sahula, Dr. Nikul Shah and Mr. Nilesh Ranpura participated in the discussion.



SESSION IS HOSTED BY
MANIPAL UNIVERSITY JAIPUR
(University under Section 2(f) of the UGC Act)

NXP Campus Connect

NXP Campus Connect 2025
Kick Off - Semiconductors Industry Trends

LIVE SESSION

In this session Hitesh Garg, Vice President & India Managing Director will be inaugurating NXP Campus Connect 2025 program. Followed by his address on Semiconductors Industry Trends. Focusing on latest shift in automotive and industrial technologies. How these technologies are shaping up future of semiconductor industry.

In the second half of the session Preet Yadav will be discussing on NXP Campus Connect 2025 details of the session for the first half of 2025, followed by an open house of Q & A.

SPEAKERS

HITESH GARG
Vice President & India Managing Director
NXP Semiconductors

PREET YADAV
Head India Innovation Ecosystem
NXP Semiconductors

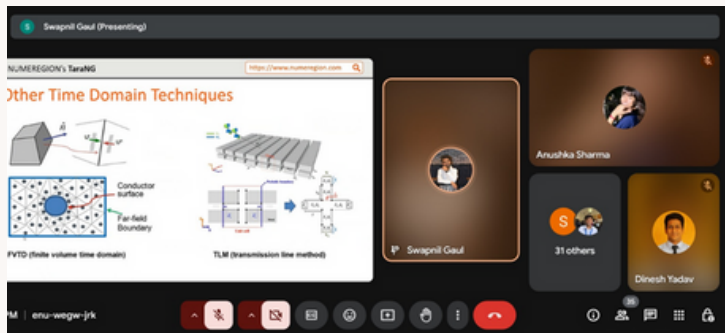
Attend the session in-person
21st JANUARY 2025 **TUESDAY 5:00-6:00 PM IST** **Sharda Pai Audi. Academic Block 2**

JOIN VIRTUALLY

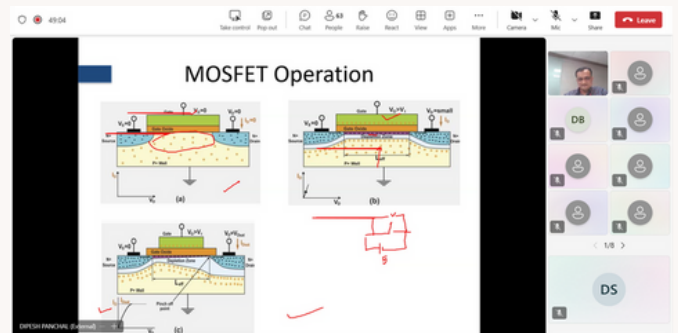
Write to us at: nxpcampus.connect@nxp.com
Connect with us at:
Website: www.nxp.com
LinkedIn: <https://www.linkedin.com/in/nxpcampusconnect/>
YouTube: <https://www.youtube.com/@NXPIndiaCommunication>

NXP Campus Connect Program, the first calendar event for 2025 was successfully hosted at Manipal University Jaipur on 21st January 2025! The event featured insightful discussions, including a compelling talk by Hitesh Garg, Vice President & India Managing Director NXP Semiconductors who shared his expertise on Software-Driven Vehicles and their transformative impact on the automotive industry.

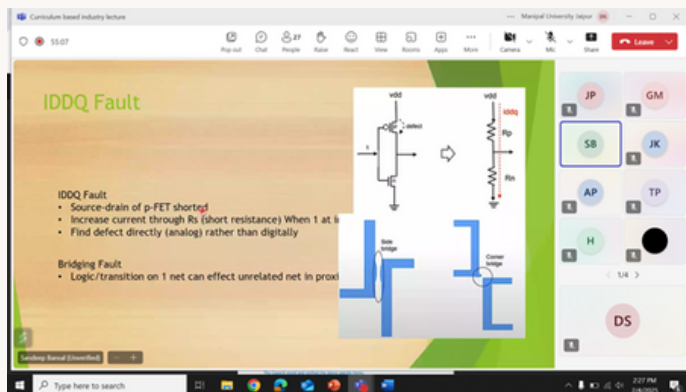
Industry Lectures



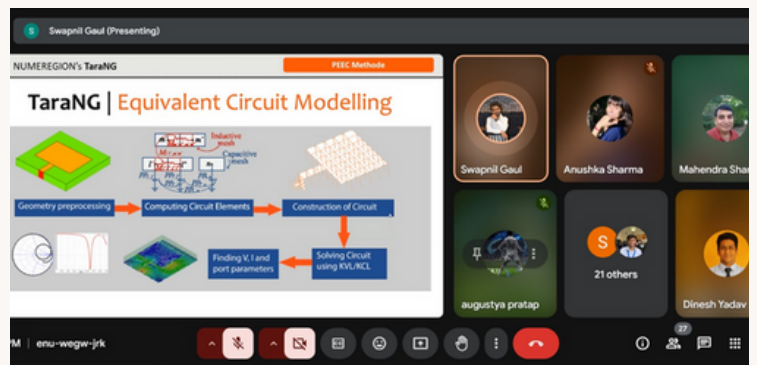
Curriculum based industry Lecture on Numerical methods for electromagnetic waves for Microwave theory course by Mr Swapnil Gaur from Numeregion Technology.



Curriculum based industry Lecture on Electronic Devices-II by Dr Dipesh Panchal from eInfochips, Ahmedabad.



Curriculum based industry Lecture on VLSI Testing and Testability by Mr Sandeep Bansal from Renesas Electronics.



Curriculum based industry Lecture on Different Numerical Methods and How they are implemented in TaraNG Electromagnetics Simulation Tool for Electromagnetic Field Theory course by Mr Swapnil Gaur from Numeregion Technology.

VALUE ADDED COURSES

VLSI SoC Design using Verilog HDL

Duration of course offered 30 hours
BY



Maven Silicon
Centre of excellence in VLSI

Device Fabrication

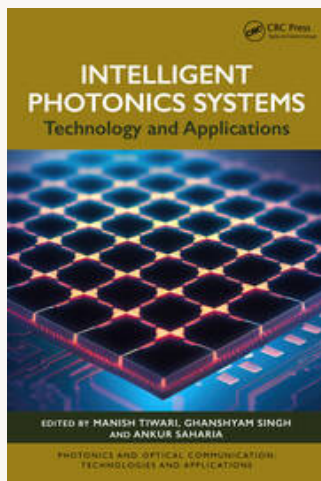
1. Hand-on experience on cleaning and Drying of Silicon Wafer.
2. Hand-on experience on oxidation process.
3. Hand-on experience on Photolithography process.
4. Hand-on experience on Diffusion process.
5. Hand-on experience on Metallization process.
6. Hand-on experience on semiconductor device testing method.

STUDENTS' ACHIEVEMENTS



25 students have successfully completed the "VLSI SoC Design Using Verilog HDL" certification course offered by Maven Silicon.

Anushka Sharma DA Contest Tech Team Srivani K Anvesh Katta Altair Technical Team towards winning Consolation Prize in Altair Antenna Design Contest



Edited book by Prof. Manish Tiwari and Dr Ankur Saharia with Dr Ghanshyam Singh, published by CRC Press, Taylor and Francis Group



Mr.Hemendra Talesara, NACD.DC, M.IOD, DDN.QTE the appointed as Adjunct Faculty at Department of ECE, MUJ. He also delivered an insightful Invited Talk and for an Interactive Discussion at Manipal University Jaipur.

MUJ Excellence Awardees 2024



Dr.Tarun Dubey



Dr.Vishal Das



Dr.Deepika Bansal



Dr.Rohit Mathur



Mr Rahul Bohra



STUDENT PLACEMENT

MANIPAL UNIVERSITY
JAIPUR
Department under Section 22F of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

SIDDHANT SHARMA
B.Tech. ECE
Batch: 2021-2025

For getting placement in
'Secure Meters'

MANIPAL UNIVERSITY
JAIPUR
Department under Section 22F of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

PUKHRAJ SINGH
B.Tech. ECE
Batch: 2021-2025

For getting placement in
'Tredence'

MANIPAL UNIVERSITY
JAIPUR
Department under Section 22F of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

SWEATA CHAKRABORTY
B.Tech. ECE
Batch: 2020-2024

For getting placement in
'Capgemini Technology
Services India Limited'

MANIPAL UNIVERSITY
JAIPUR
Department under Section 22F of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

ABHIJEET SACHAN
B.Tech. ECE
Batch: 2020-2024

For getting placement in
'Teachnook'

MANIPAL UNIVERSITY
JAIPUR
Department under Section 22F of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

AMAL BIJU
B.Tech. ECE
Batch: 2020-2024

For getting placement in
'STATCON POWTECH'

MANIPAL UNIVERSITY
JAIPUR
Department under Section 22F of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

NUPUR PALAV
B.Tech. ECE
Batch: 2020-2024

For getting placement in
'TVS Motor'

MANIPAL UNIVERSITY
JAIPUR
Department under Section 22F of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

MANSI AGRAWAL
B.Tech. ECE
Batch: 2021-2025

For getting placement in
'Tech Mahindra'

MANIPAL UNIVERSITY
JAIPUR
Department under Section 22F of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

SHRADDHA DAYAL
B.Tech. ECE
Batch: 2021-2025

For getting placement in
'Infosys'

MANIPAL UNIVERSITY
JAIPUR
Department under Section 22F of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

NEHA GARG
B.Tech. ECE
Batch: 2020-2024

For getting placement in
'Secure Meters'

MANIPAL UNIVERSITY
JAIPUR
Department under Section 22F of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

MUSKAN SINGH
B.Tech. ECE
Batch: 2020-2024

For getting placement in
'Sabre'

Editorial Team

Dr Neha Singh. Dr Ankur Saharia

Mr. Ankit Saini (23FE10ECE00041), Mr Harshit Jatwa (23FE10EVL00013)